Spring 2007           Syllabus
Electronics I – Physics 252: Introduction to Analog Circuits

Instructors
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Office hours (held in room 148):
Monday  2-3:30 pm (Aubin)
Thursday 2-3:30 pm (Yang)

Course Objectives
The primary purpose of this course is to teach you how to design basic analog
electronic circuits for connecting one device to another properly and efficiently: this is
generally the function of most lab-built electronic circuits.
Along the way, we will also learn how to do basic analog circuit design and to a
lesser extent signal acquisition and detection. More specifically, you will learn about the
following essential concepts:
-  impedance
-  amplification
-  frequency analysis
-  feedback

You will also learn to use the following components and equipment:
-  resistors, capacitors, inductors.
-  diodes, photo-diodes, transistors, FETs.
-  Op-amps, comparators.
-  Multimeters, oscilloscopes, function generators.
-  Breadboards and soldering irons.
-  Modern circuit design and lay-out software.

Texts
There is no official textbook for the course. I will be posting introductory chapters
and laboratories created specifically for the course on my website before the lecture.
These chapters and labs were originally created by Prof. Jeff Nelson and Prof. Bill Cooke
and have been adapted to the current course. While there are many good electronics
textbooks, I strongly recommend that you consult the following book frequently for

**Class Format**

The class hours are divided into two parts: Lecture and Lab. The lecture will be on Monday 1:00-1:50 pm in room 238, and will cover the concepts to be tested in the lab later in the week. The lab portion of the class will be held on Tuesday 2:00-4:50 pm for Physics 252-02 (20736) and Wednesday for Physics 252-01 (20735).

The labs will be open from 1:00 pm to 6:00 pm for students looking to start early or finish late.

Most labs will include a design component. The designs should be prepared prior to attending lab so as to finish the lab measurements on time.

**Evaluations**

Your final grade for the course will be determined from the following grading weight distribution:

- Notebooks/Lab: 30%
- Quizzes/Participation: 20%
- Lab Reports: 30%
- Final: 20%

**Notebooks**

Your lab book should be a composition style notebook with either line or quadrangle ruling or a computation logbook. It can be obtained at most stationary stores (i.e. campus bookstore, Staples, etc …)

Your lab book is the primary record of your work and data. You should record everything that you do in the lab book, so that anyone (such as the instructors and yourself) can understand what you have done and measured. You should include circuit diagrams, observations, questions, answers, design considerations, measurement data, and analysis. Diagrams, data, graphs, and other notes on separate pieces of paper should be glued, taped, or stapled into the lab book. Generally, you cannot write too much down.

The lab book will be graded on completeness and neatness. It should also feature a table of contents. The lab books will be turned in roughly every two weeks and returned before the next lab.

**Lab reports**

Scientists and engineers communicate their activities and research results through short reports. Each student is required to turn in a report for each lab. The lab report is due by the following Monday, either in class or at my office.
The lab report should present what you did in the lab, or some aspect of what you did as long as it encompasses the main theme of the lab. The reports should have the following characteristics and components:
- Typed or printed.
- Short report (max 3 pages single space, but shorter is better).
- Structured with an introduction, a main body, and a conclusion.
- Measurement data should be included in tables and plots.
- All data should be analyzed and interpreted.
- Important measured numbers should include a justified error bar.

Quizzes
There will be frequent 5 minute quizzes at the beginning of lecture and lab to encourage you to review concepts and circuit design.

Final exam
There will be a final exam on May 10 covering all course material.
Weekly Topics

**Week 0:** 1/22-24  NO CLASS

**Week 1:** 1/29-31  Ohm’s Law
Ohm’s Law, Power, network analysis, voltage divider, measurements

**Week 2:** 2/5-7  Thevenin’s Theorem
Impedance Matching, Thevenin’s Theorem

**Week 3:** 2/12-14  Complex Impedance
Capacitors, inductors, complex impedance, transformers, resonators

**Week 4:** 2/19-21  Signals
Filters (RC, Chebychev, Butterworth, etc…), coaxial cables, AM and FM modulation, ground loops

**Week 5:** 2/26-28  Diodes
Diodes, rectifiers, photo-diodes

**Week 6:** 3/5-7  Transistors 1
Transistors 1: BJTs, gain, amplifiers

--------------------------------- Spring Break ---------------------------------

**Week 7:** 3/19-21  Transistors 2
Transistors 2: BJTs continued, amplifiers, FETs

**Week 8:** 3/26-28  Transistors 3
Transistors 3: FETs continued, amplifiers

**Week 8:** 4/2-4  Op-Amps 1
Op-amps 1: Golden rules of op-amps, integrated circuits, simple circuits

**Week 9:** 4/9-11  Op-Amps 2
Op-amps 2: Op-amp limitations, important circuits

**Week 10:** 4/16-18  Control Theory
Feedback: Control theory, PID control

**Week 11:** 4/23-25  Complex Circuits
Important complex op-amp circuits (comparators, triggers, etc …)

**Week 12:** 4/30-5/2  Modern Design Tools
Design tools (Spice and Eagle).

**Week 13:** 5/10  (Thursday, 1:30-4:30)  FINAL EXAM